

## **POST-EXAMINATION CORRECTION**

Thursday 26 May 2016 – Afternoon

### **AS GCE ELECTRONICS**

#### **F612/01 Signal Processors**

We have provided a correction to the published examination paper to make it more suitable for practice material. Details are provided below:

In the timing diagram (**Fig.6.2**) in **Question 6 part (b)**, the line indicating the state of element **A** should have been positioned at '0' rather than '1'.

Any enquiry about this notice should be referred to the Customer Contact Centre on 01223 553 998 or [general.qualifications@ocr.org.uk](mailto:general.qualifications@ocr.org.uk)