

# **Tuesday 11 January 2022 – Afternoon**

# Level 1/2 Cambridge National in Systems Control in Engineering

R113/01 Electronic principles

Time allowed: 1 hour

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You must have:	
<ul> <li>a scientific or graphical calculator</li> </ul>	

Please write clearly in black ink. <b>Do not write in the barcodes.</b>								
Centre number					Candidate number			
First name(s)								
Last name								

#### **INSTRUCTIONS**

- Use black ink. You can use an HB pencil, but only for graphs and diagrams.
- Write your answer to each question in the space provided. If you need extra space use the lined pages at the end of this booklet. The question numbers must be clearly shown.
- · Answer all the questions.

#### **INFORMATION**

- The total mark for this paper is **60**.
- The marks for each question are shown in brackets [ ].
- Quality of written communication will be assessed in questions marked with an asterisk (\*).
- This document has 8 pages.

## **ADVICE**

· Read each question carefully before you start your answer.

## Answer all the questions.

			·	
(a)	(i)	Complete the table by using words from Each word may be used once or not a		
		Automatic		
		Continuous		
		Portable		
		Sustainable		
		Unsustainable		
		Power Source	Type of Power Source	
		6 V Battery		
		1kW Solar Panel		
		230 V AC Mains		
	(ii)	Give <b>one</b> drawback of using a sustain	nable power source.	[3]
(	iii)	Give <b>one</b> example of a combined pov	wer source for portable equipment.	[1]
(b)		culate the potential difference across	a heating element if its resistance is	· 700 and the
	00	ent flowing through it is 3.3A.		s 70s2 and the
		ent flowing through it is 3.3A.		

.....[4]

2 Fig. 1 shows part of a circuit diagram with an LED rated at 2 V 20 mA.

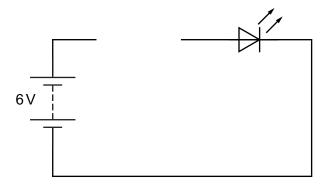


Fig. 1

(a)	Complete the circuit diagram in Fig. 1 by drawing a resistor in the space provided.	[1]
(b)	Calculate the potential difference across the resistor.	
		[2]
(c)	Give <b>two</b> reasons for having a resistor in the LED circuit.	
	1	
	2	[2]
		[-]
(d)	Describe the operation of an LED.	
		[2]
(e)	Calculate a suitable value for the resistor if the current flow required is 20 mA.	
		[3]

3 (a) Complete the table, using a tick (✓) against **three** techniques that can be used to identify potential electrical hazards.

Techniques	Tick (✓)
Portable appliance testing	
Power supply unit	
Truth tables	
Use of residual current device	
Visual inspection of equipment	

(b)	The half split method is a fault finding procedure for electronic circuits.
	Describe how this works.
	[3]
	[V]
(c)	State <b>two</b> benefits of using a virtual signal generator for testing a simulated circuit.
	1
	2
	[2]
(d)	In the space provided below, draw the shape of a signal that could be provided by a virtual signal generator.

[3]

4 (a)*	Discuss the benefits to a manufacturer of using surface mount components in electronic circuits, compared to using through hole mounted components.
	[6]
(b)	State the names of <b>three</b> manufacturing processes used within commercial circuit construction.
	1
	2
	3 <b>[3]</b>
(0)	Name are item of test equipment which is used for testing electronic circuits
(6)	Name <b>one</b> item of test equipment which is used for testing electronic circuits.

5	(a)	Identify	two	smart	modern	materials.
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1		
2	,	
		[2]

(b) Fig. 2 shows part of a circuit diagram for an astable circuit using a 555 timer.

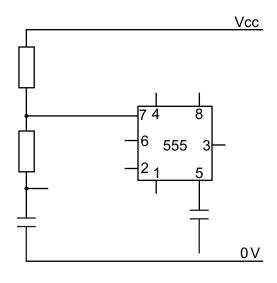


Fig. 2

Complete the circuit diagram in **Fig. 2** by drawing in the connections for pins 1, 2, 4, 5, 6, 8. **[4]** 

- (c) Draw a label on the circuit diagram in Fig. 2 to show what pin 3 is used for. [1]
- (d) Complete the sentence using words from the list below: Each word may be used once or not at all.

external

internal

irregular

regular

sawtooth

square

An astable circuit will produce a ...... signal in the form of a ...... trigger. [3]

6 (a) The block diagram in Fig. 3 shows an alarm system for heat and smoke.

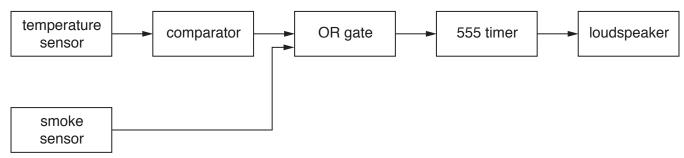


Fig. 3

	(i)	State the names of the <b>two</b> blocks that will give an input to the system.	
	(ii)	State which block of the system represents the output stage.	
(	(iii)	State in which block an operational amplifier is used.	
(	(iv)	State in which block a thermistor is used.	
(b)	Des	cribe what is meant by the term 'capacitor voltage rating'.	
(c)		culate the maximum and minimum value a capacitor will have if it is rated at 120 μF ± 10 imum value	
	Mini	mum value	
			 [3]

#### ADDITIONAL ANSWER SPACE

If additiona must be cle	I space is required, you should use the following lined page(s). arly shown in the margin(s).	The question number(s)



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