

# **Cambridge Technicals Engineering**

## **Unit 4: Principles of electrical and electronic engineering**

Level 3 Cambridge Technical Certificate/Diploma in Engineering  
**05822 - 05825 & 05873**

## **Mark Scheme for June 2022**

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This mark scheme is published as an aid to teachers and students, to indicate the requirements of the examination. It shows the basis on which marks were awarded by examiners. It does not indicate the details of the discussions which took place at an examiners' meeting before marking commenced.

All examiners are instructed that alternative correct answers and unexpected approaches in candidates' scripts must be given marks that fairly reflect the relevant knowledge and skills demonstrated.

Mark schemes should be read in conjunction with the published question papers and the report on the examination.

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## MARKING INSTRUCTIONS

### PREPARATION FOR MARKING

#### TRADITIONAL

Before the Standardisation meeting you must mark at least 10 scripts from several centres. For this preliminary marking you should use **pencil** and follow the **mark scheme**. Bring these **marked scripts** to the meeting.

### MARKING

1. Mark strictly to the mark scheme.
2. Marks awarded must relate directly to the marking criteria.
3. The schedule of dates is very important. It is essential that you meet the traditional 40% Batch 1 and 100% Batch 2 deadlines. If you experience problems, you must contact your Team Leader (Supervisor) without delay.
4. If you are in any doubt about applying the mark scheme, consult your Team Leader by telephone or by email.

5. **Crossed Out Responses**

Where a candidate has crossed out a response and provided a clear alternative then the crossed out response is not marked. Where no alternative response has been provided, examiners may give candidates the benefit of the doubt and mark the crossed out response where legible.

**Multiple Choice Question Responses**

When a multiple choice question has only a single, correct response and a candidate provides two responses (even if one of these responses is correct), then no mark should be awarded (as it is not possible to determine which was the first response selected by the candidate). When a question requires candidates to select more than one option/multiple options, then local marking arrangements need to ensure consistency of approach.

**Contradictory Responses**

When a candidate provides contradictory responses, then no mark should be awarded, even if one of the answers is correct.

**Short Answer Questions** (requiring only a list by way of a response, usually worth only **one mark per response**)

Where candidates are required to provide a set number of short answer responses then only the set number of responses should be marked. The response space should be marked from left to right on each line and then line by line until the required number of responses have been considered. The remaining responses should not then be marked. Examiners will have to apply judgement as to whether a 'second response' on a line is a development of the 'first response', rather than a separate, discrete response. (The underlying assumption is that the candidate is attempting to hedge their bets and therefore getting undue benefit rather than engaging with the question and giving the most relevant/correct responses.)

**Short Answer Questions** (requiring a more developed response, worth **two or more marks**)

If the candidates are required to provide a description of, say, three items or factors and four items or factors are provided, then mark on a similar basis – that is downwards (as it is unlikely in this situation that a candidate will provide more than one response in each section of the response space.)

**Longer Answer Questions** (requiring a developed response)

Where candidates have provided two (or more) responses to a medium or high tariff question which only required a single (developed) response and not crossed out the first response, then only the first response should be marked. Examiners will need to apply professional judgement as to whether the second (or a subsequent) response is a 'new start' or simply a poorly expressed continuation of the first response.

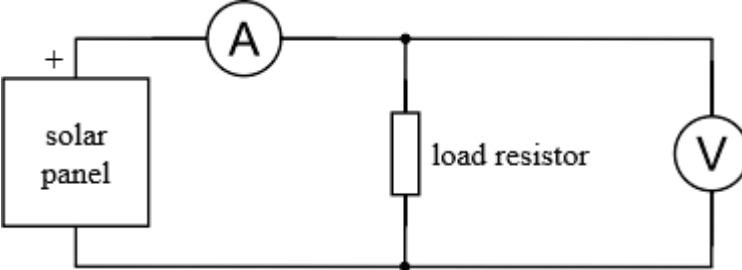
6. Always check the pages (and additional lined pages if present) at the end of the response in case any answers have been continued there. If the candidate has continued an answer there then add an annotation to confirm that the work has been seen.
7. There is a NR (No Response) option. Award NR (No Response)
  - if there is nothing written at all in the answer space
  - OR if there is a comment which does not in anyway relate to the question (e.g. 'can't do', 'don't know')
  - OR if there is a mark (e.g. a dash, a question mark) which isn't an attempt at the questionNote: Award 0 marks - for an attempt that earns no credit (including copying out the question)
8. Assistant Examiners will email a brief report on the performance of candidates to your Team Leader (Supervisor) by the end of the marking period. Your report should contain notes on particular strength displayed as well as common errors or weaknesses. Constructive criticism of the question paper/mark scheme is also appreciated.

## 9. Annotations

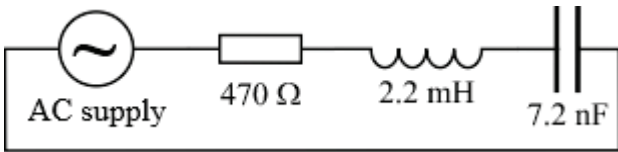
<b><i>Annotation</i></b>	<b><i>Meaning</i></b>
tick	Correct response
cross	Incorrect response
Omission mark (carat)	Incomplete response
ECF	Error carried forward
BOD	Benefit of doubt
NBOD	No benefit of doubt
RE	Rounding error

## 10. Subject-specific marking instructions

- In all numerical calculation questions a correct response will gain all marks unless specified otherwise.
- Rounding of answers should be to the same number of significant figures as the data in the question, or, otherwise, an answer will be correct provided it rounds to the correct answer.
- Symbols used in circuit diagrams must identify relevant components uniquely and unambiguously.

Question		Answer	Marks	Guidance
1	(a)	<p>Ammeter in series circuit with solar panel and load resistor                      Voltmeter in parallel with load resistor</p> 	<p>1 1</p>	
1	(b)	$V = IR$ $I = 150 \text{ mA} = 0.15 \text{ A}$ $V = 0.15 \times 38$ $V = 5.7 \text{ V}$	<p>1 1</p>	<p>Evidence of correct conversion from mA to A.                      Synoptic mark from unit 2: 1.1                      Correct calculation (allow ecf)</p>
1	(c)	$P = I^2 R$ $P = 0.15^2 \times 38$ <p style="text-align: center;">OR</p> $P = IV$ $P = 0.15 \times 5.7$ $P = 0.855 \text{ W}$	<p>1</p>	<p>Synoptic mark from unit 2: 3.8                      Allow ecf from 1b</p>

Question			Answer	Marks	Guidance
1	(d)	(i)	$P = I^2 R$ $\therefore I = \sqrt{P/R}$  From graph when $27 \Omega$ then $P = 0.91 \text{ W} (\pm 0.005 \text{ W})$  $I = \sqrt{(0.91/27)}$ $= 0.1835 (\pm 0.0005) \text{ A}$	1	Evidence of correct method. Synoptic mark from unit 1: 1.4
				1	Evidence of finding correct value from graph. Accept values in range 0.905 – 0.915 W.
				1	Correct answer. Accept answers to 2 s.f. (i.e. 0.18 A)
1	(d)	(ii)	$22 \Omega$	1	
1	(d)	(iii)	Graph shows <u>maximum power/peak power/graph peaks</u> at load resistance of $22 \Omega$ (wtte)	1	
			Maximum power when <u>load resistance equals internal resistance</u> (wtte)	1	

Question			Answer	Marks	Guidance
2	(a)	(i)	$f = 20 \text{ kHz} = 20000 \text{ Hz}$ $L = 2.2 \text{ mH} = 0.0022 \text{ H}$  $X_L = 2\pi fL = 2\pi \times 20000 \times 0.0022$ $X_L = 276 \Omega$	1	Evidence of correct conversion from kHz to Hz and mH to H
				1	Correct answer ecf conversion to Hz and/or H
2	(a)	(ii)	$Z = \sqrt{R^2 + X_L^2}$ $Z = \sqrt{R^2 + X_L^2}$ $Z = \sqrt{470^2 + 276^2}$ $Z = 545 \Omega$	1	Evidence of correct equation used (ecf from 2ai)
				1	Correct answer (allow ecf)
2	(b)	(i)		1	Correct component symbols in series. Components can be in any order.
2	(b)	(ii)	$7.2 \text{ nF} = 7.2 \times 10^{-9} \text{ F}$ or $0.0000000072 \text{ F}$	1	Synoptic mark from unit 2: 1.1



Question			Answer				Marks	Guidance
2	(b)	(iii)	Frequency	Reactance of inductor	Reactance of capacitor	Impedance of series RLC circuit	4	One mark for each correct answer Accept answers $\pm 5 \Omega$
			$f / \text{Hz}$	$X_L / \mu \Omega$	$X_C / \mu \Omega$	$Z / \mu \Omega$		
			25000	346	884	$Z = \sqrt{R^2 + (X_C - X_L)^2}$ $Z = \sqrt{470^2 + (884 - 346)^2}$ <b>714</b>		
			40000	553	$Z = R$ when $X_C = X_L$ <b>553</b>	470		
50000	$X_L = 2 \mu f L$ $X_L = 2 \mu \times 50000 \times 0.0022$ <b>691</b>	442	$Z = \sqrt{R^2 + (X_L - X_C)^2}$ $Z = \sqrt{470^2 + (691 - 442)^2}$ <b>532</b>					
3	(a)		<b>shunt-wound</b>				1	
3	(b)		Either: <ul style="list-style-type: none"> <li>• low <b>starting</b> torque needed for a fan (wtte)</li> <li>• fan runs at [near] constant speed (wtte)</li> <li>• speed of shunt-wound motor limited (wtte)</li> </ul>				1	
3	(c)		$V = E + I_a R_a$				1	Correct equation
			$E = V - I_a R_a$				1	Correct answer
			$E = 12 - 2.2 \times 0.9$ $E = 10.02 = 10$ (2 s.f.) Units: V				1	Correct units. Synoptic mark from unit 2: 1.1

Question		Answer	Marks	Guidance
3	(d)	$R_f = V/I$ $R_f = 12/1.4$ $R_f = 8.571 = 8.6 \Omega$ (2 s.f.)	1	
3	(e) (i)	<p>One mark for each. This order only.</p> <p><b>reduced</b>  <b>reduced</b>  <b>increased</b></p>	3	<p>e.g.</p> <p>When the switch SW1 is closed the current in the field winding is 1.4 A and the motor spins. When the switch SW1 is opened the resistor, R, is in series with the field winding. Therefore, the current in the field winding (<math>I_f</math>) is <b>reduced</b> and the magnetic flux (<math>\phi</math>) in the motor is <b>reduced</b>. This means that the speed of the motor is <b>increased</b>.</p>
3	(e) (ii)	$I_f = V/(R+R_f)$ $I_f = 12/(10+8.571)$ ecf from d $I_f = 0.6462 = 0.65$ A (2 s.f.)	1  1	<p>Correct method i.e. evidence of adding of adding resistor value to resistance of field winding.</p> <p>Correct answer</p>
4	(a)	<p>One mark for each correct block</p> <pre> graph LR     A[transformer] --&gt; B[rectifier]     B --&gt; C[smoothing circuit]     C --&gt; D[stabilising circuit]           </pre>	3	
4	(b)	Converts from alternating current (wtte) to direct current (wtte)	1 1	

Question		Answer	Marks	Guidance
4	(c)	<p>Either:</p> <ul style="list-style-type: none"> <li>• Diode <u>only</u> conducts/allows current to flow when the power supply is correctly connected. (wtte)</li> </ul> <p>OR</p> <ul style="list-style-type: none"> <li>• Diode prevents current flowing when power supply is not connected the correct way around. (wtte)</li> </ul> <p>OR</p> <ul style="list-style-type: none"> <li>• Idea that diode only conducts in one direction/only allows current to flow in one direction</li> </ul>	1	Synoptic mark from unit 2: 3.3
4	(d)	<p><b>One mark for each. This order only.</b></p> <p><b>powering the wireless router</b>  <b>powering the wireless router OR rectifying the supply</b>  <b>being charged</b></p>	3	<p>e.g.</p> <p>When the AC mains supply is not working, the battery backup power supply is <b>powering the wireless router</b>. When the AC mains supply is working, the stabilised power supply is <b>powering the wireless router/rectifying the supply</b> and battery backup power supply is <b>being charged</b>.</p>



Question		Answer	Marks	Guidance
6	(a)	<p>One mark for each. This order only</p> <p><b>Clock</b>  <b>changed from low to high</b></p> <p><b>D</b>  <b>held high</b></p>	4	<p>e.g.</p> <p>A rising edge triggered D-type flip flop is triggered when the <b>Clock</b> connection is <b>changed from low to high</b>. To make Q high, the <b>D</b> connection is <b>held high</b> whilst the D-type flip flop is triggered.</p>
6	(b)	<p>One mark for each correct line.</p> <p>Statements about logic gates</p> <p>The output is only high when all the inputs are low.</p> <p>The output is only high when only one of the inputs is high.</p> <p>The output is only low when all the inputs are high.</p> <p>Names of logic gates</p> <p>AND gate</p> <p>NAND gate</p> <p>NOR gate</p> <p>OR gate</p> <p>XOR gate</p>	3	

Question		Answer	Marks	Guidance																																				
6	(c)	<p>One mark for each correct column</p> <table border="1"> <thead> <tr> <th>D</th> <th>E</th> <th>F</th> <th>G</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> </tr> </tbody> </table>	D	E	F	G	1	0	1	0	1	1	0	0	1	1	0	0	1	1	0	0	1	0	1	0	1	1	0	0	0	1	0	1	0	1	0	1	4	ecf F from E ecf G from D & F
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